

**Notice of References Cited**

Application/Control No.

09/517,518

Applicant(s)/Patent Under  
Reexamination  
WONG ET AL.

Examiner

A. M. Thompson

Art Unit

2825

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number	Date	Name	Classification	
		Country Code-Number-Kind Code	MM-YYYY			
	A	US-6,295,636 ✓	09-2001	Dupenloup	716	18
	B	US-5,956,257 ✓	09-1999	Ginetti et al.	716-	3
	C	US-6,292,931 ✓	09-2001	Dupenloup	716	18
	D	US-5,933,356 ✓	08-1999	Rostoker et al.	703	15
	E	US-6,289,498 ✓	09-2001	Dupenloup	716	18
	F	US-				
	G	US-				
	H	US-				
	I	US-				
	J	US-				
	K	US-				
	L	US-				
	M	US-				

**FOREIGN PATENT DOCUMENTS**

*		Document Number	Date	Country	Name	Classification	
		Country Code-Number-Kind Code	MM-YYYY				
	N						
	O						
	P						
	Q						
	R						
	S						
	T						

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	David R. Smith, Hardware Synthesis from Encapsulated Verilog Modules, Application Specific Systems, Architectures and Processors. ASAP 96. Proceedings of International Conference on
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.